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T-757 P18,20 U-331

#14/8-13-82
JR

GR 98 P 1507 P

MA
#14

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Rusch et al.
Appl. No. : 09/685,361
Filed : October 10, 2000
Title : Semiconductor Memory Device and Method for
Fabricating It
Examiner : Lynne Gurley
Group Art Unit : 2812

FAX COPY RECEIVED

AUG 8 2002

REQUEST TO APPROVE DRAWING CHANGES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

TECHNOLOGY CENTER 2800

S i r :

Please approve the drawing changes to FIG 1 and FIG 2 which
are marked in red on the accompanying drawing page 1/2. A
formal version is also enclosed.

Respectfully submitted,

LAURENCE A. GREENBERG
REG. NO. 29,308

For Applicants

YHC:cgm

Date: August 8, 2002

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